

Date: 16/12/2021

Minutes of Meeting – Board of Study (BOS)

Minutes of Meeting of Board of Study of Computer Science and Design (CSD) held on 16th December 2021 at 2.30 P.M in IQAC, MG Block.

Member Present:

S. No.	Name of the Faculty	Designation	Signature
1	Dr. Govinda Rajulu G, Professor and HOD (CSD)	Chairman	G. Govinda Rajulu
2	Dr. V. Kamakshi Prasad, Professor of CSE, JNTUH College of Engineering	University Nominee	V. Kamakshi Prasad
3	Dr. K. Venkatesh Sharma, Professor, Dept. of CSE, CVR College of Engg, Hyderabad	Educationist	K. Venkatesh Sharma
4	Dr. P. L. Srinivasa Murthy, Professor Department of Computer Science & Engineering, Institute of Aeronautical Engineering, Dundigal, Hyderabad	Educationist	P. L. Srinivasa Murthy
5	Mr. Bonthala Mallikarjuna Aswanth Kumar, Lead Technology, Synechron	Industrialist	B. M. Aswanth Kumar
6	Dr. S.V.S Ramakrishnam Raju (Dean-Academics)	Faculty Member	S. V. S. Ramakrishnam Raju
7	Dr. M. Narayanan (Professor)	Faculty Member	M. Narayanan
8	Dr. R. Nagaraju (Professor)	Faculty Member	R. Nagaraju
9	Dr. T. Poongothai (Professor)	Faculty Member	T. Poongothai
10	Dr. D. Ranadheer Reddy (Professor)	Faculty Member	D. Ranadheer Reddy
11	Dr. N. Satheesh (Professor)	Faculty Member	N. Satheesh
12	Mr. Pannati Nagesh (React Front End Developer)	Alumni of CSE	P. Nagesh

The meeting began with Chairman, Board of Studies extending a warm welcome to all the members of participating in the meeting.

a) The following points were discussed and approved during the meeting

1. The syllabi and course structure for first and second year were presented, discussed and approved.

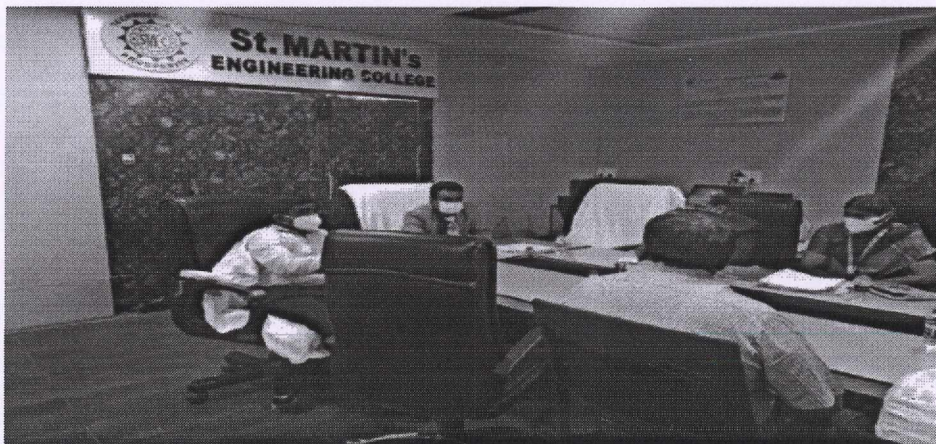
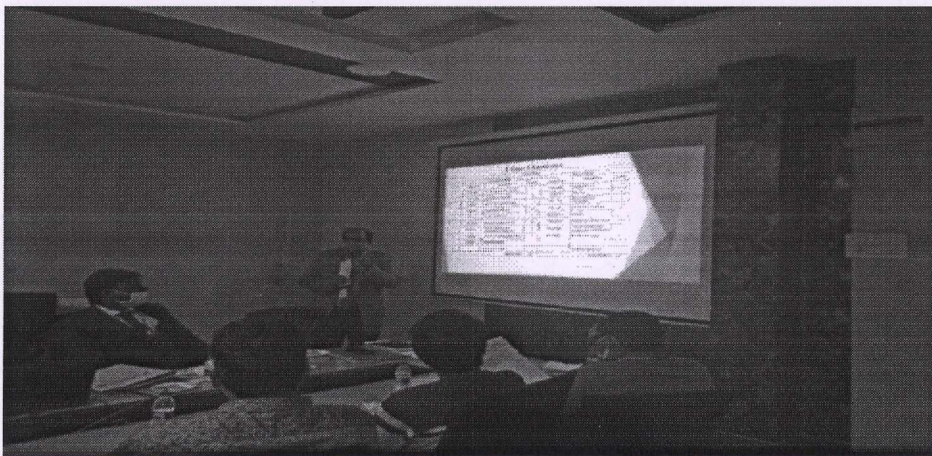
S.No.	Theory	Laboratory
I Year II Semester		
1	1. Programming for Problem Solving	1. Programming for Problem Solving Lab
II Year I Semester		
2	1. Analog and Digital Electronics 2. Data Structures 3. Mathematical and Statistical Foundations 4. Computer Vision 5. Python Programming	1. Analog and Digital Electronics Lab 2. Data Structures Lab 3. IT Workshop Lab 4. Python Programming Lab 5. Gender Sensitization Lab
II Year II Semester		
3	1. Discrete Mathematics 2. Computer Graphics 3. Operating Systems 4. Database Management System 5. Java Programming 6. Constitution of India	1. Computer Graphics Lab 2. Database Management Systems Lab 3. Java Programming Lab

2. The course structure of 3-1, 3-2 and 4-1, 4-2 semester is presented and discussed.
3. The total credits for the programme were discussed and finalized.

b) The following points were suggested for future possible implementations

1. Suggested that in Physics and Chemistry subjects, topics to be kept which will have more relevancy with computer science like quantum computing and other emerging CSE allied areas.
2. Data Mining subject need to be included as a core subject.
3. Suggested to include the GATE and placement opportunity oriented subjects in the syllabus.
4. Suggested to include the subjects like Distributed Systems, Mobile Application Development, Web Technologies and Distributed Computing.
5. Suggested for the faculty to undergo MOOCs/NPTEL course on Quantum Computing and other thrust areas of computer science.

The meeting ended with chairman thanking members for their lively and useful interaction to evolve a best possible course structure and syllabus for the B. Tech Computer Science and Design (CSD) programme.



Copy to:
1. Principal
2. IQAC

G. Govinda Rajulu
HOD (CSD)

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